



PATENT  
8001-1191

IN THE U. S. PATENT AND TRADEMARK OFFICE

In re application of

Koji ARITA et al. Conf. 8942

Application No. 10/777,198 Group 2812

Filed: February 13, 2004 Examiner Renne R. Berry

Title: METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE HAVING  
DAMASCENE INTERCONNECTION

**AMENDMENT**

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 7, 2005

Sir:

In response to the Office Action mailed October 7, 2004, for which the period for response has been extended three months, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Amendments to the Drawings** begin on page 7 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

**Remarks** begin on page 8 of this paper.

An **Appendix** is attached following the signature page of this paper.